Firmware datasheet

1 Firmware

This document is the technical datasheet of a communication firmware. It describes the structure, features and interfaces of a loadable communication firmware, running on a netX SoC based device. Each communication firmware consists of several software components. The document lists the technical data and limitations of each component separately. Furthermore 3rd party software components and their licenses are listed in this document.

A Loadable Firmware (LFW) is provided as single binary file (*.nxf, *.nxi) or set of binaray files (*.nxi + *.nxe). Each binary, respectively set of binary files, comes with a dedicated firmware datasheet document.

More information with a higher level of details, can be found in various additional documents. Primarily the Dual Port Memory Manuals and the Protocol API Manuals.

1.1 File information

Firmware	Single port EtherNet/IP Adapter firmware
File name	X170H000.nxf
Version	V3.8.0.3

Table 1. File information

1.2 Requirements

For operation, the firmware requires the following hardware environment and parameters.

Requirements	Description
ASIC	netX52
Hardware design	Hardware design according to "netX52 Design-In Guide" .
2nd Stage Loader	V1.6.0.0 or newer
Device data	Device data provided by the security memory or via DDP - Device Data Provider mailbox service
MAC addresses	MAC 0: EtherNet/IP and IP communication MAC 1: LLDP communication

Table 2. Requirements

2 Firmware interfaces and features

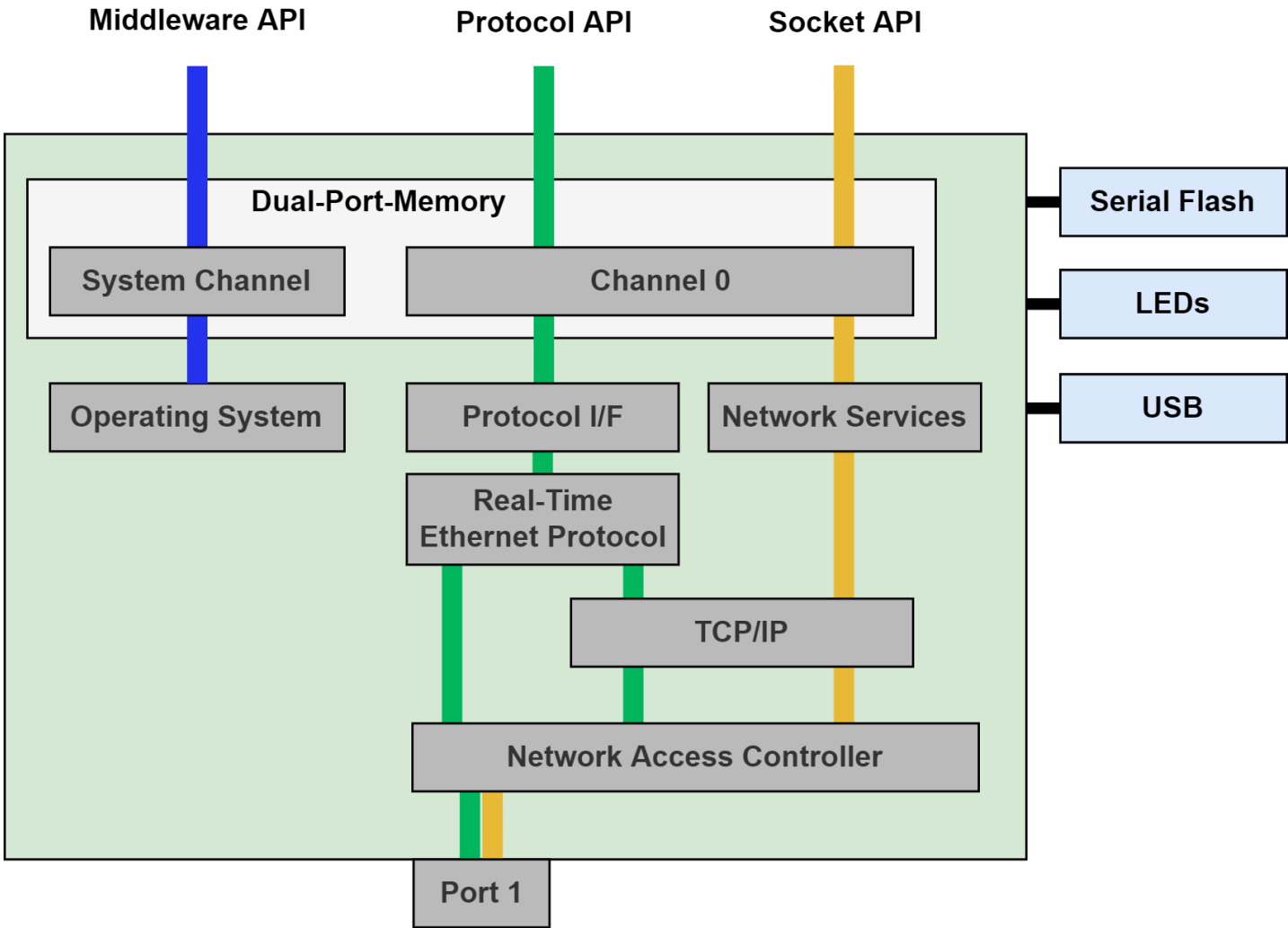The following figure illustrates the internal structure and all available interfaces of the firmware.

Figure 1. Firmware structure

2.1 Dual-Port-Memory

The Dual Port Memory (DPM), physically located inside the netX SoC, is the central interface between the communication firmware and the user application. It is physically accessed by an external parallel bus, serial SPI interface or an internal data bus. The DPM features a linear address space. The address space is partitioned into several consecutive sections, called "channels". The user application and the communication firmware exchange data, commands and notifications by reading from and writing to the DPM. Each DPM channel is mapped to dedicated firmware functionality and allows usage of respective APIs. You find detailed information about the DPM layout, address spaces and generic services in the Dual Port Memory Manuals.

Channel	API	Manual
System	Middleware API	Manual "netX Dual-Port Memory Interface DPM" and "netX Dual-Port Memory packet-based services"
Channel 0	EtherNet/IP Adapter API	Manual "EtherNet/IP Adapter"
Channel 0	Socket API	Manual "Socket Interface - Packet Interface"

Table 3. Dual-Port-Memory layout

2.2 EtherNet/IP

This firmware offers EtherNet/IP Adapter features and an API to access them.

2.2.1 Technical data

The following technical data applies to EtherNet/IP Adapter features.

Feature	Value
Max. number of input data	504 bytes per assembly instance
Max. number of output data	504 bytes per assembly instance
Max. number of assembly instances	10
IO connection types (implicit)	Exclusive owner Listen only Input only
IO connection trigger types	Cyclic Application triggered Change of state
IO connection RPI (O2T / T2O)	min. 1ms, max. 32,767ms* * Depending on the number of parallel connections and sizes of input and output data.
Explicit messages	Connected and unconnected
Unconnected Message Manager (UCMM)	Supported
Max. number of connections	Target Class 0/1: 5 Target Class 3: 7 UCMM: 7
Predefined standard objects	Identity Object (0x01) Message Router Object (0x02) Assembly Object (0x04) Connection Manager (0x06) QoS Object (0x48) TCP/IP Interface Object (0xF5) Ethernet Link Object (0xF6) LLDP Management Object (0x109)
Max. number of user-specific objects	20
Supported features	TCP/IP, UDP/IP DHCP, BOOTP Address Conflict Detection (ACD) Quality of Service CIP Reset services - Identity Object Reset service of type 0 and 1 QuickConnect LLDP, SNMP (LLDP MIB)
Ethernet Speed	10 and 100 MBit/s
Ethernet Duplex Modes	Half Duplex, Full Duplex, Auto-Negotiation
Ethernet MDI modes	MDI, MDI-X, Auto-MDIX
Data transport layer	Ethernet II, IEEE 802.3

Table 4. Technical data

2.2.2 Configuration

- by packets (e.g via Dual-Port-Memory mailbox)
- by database (two files named `config.nxd` and `nwid.nxd`) created by Communication Studio or Sycon.NET

2.2.3 Restrictions and limitations

NOT supported are:

- Tags [common mechanism to address typed PLC data using string identifiers]
- CIP Motion
- CIP Safety
  This means the protocol stack itself does not implement the safety application layer. This has to be implemented on the host application side. However, the protocol stack supports all EtherNet/IP features required to build a device that is CIP Safety capable.
- Device Level Ring (DLR)
- CIP Sync

2.3 Socket API

The Socket API provides access to the integrated IP stack. Users can implement custom or standard TCP and UDP based protocols on the application side. Both server and client applications are possible. The Socket API has a POSIX socket like interface.

2.3.1 Technical data

The following technical data apply to LWIP component.

Feature	Description
Number of possible parallel active sockets	Default: 8 Can be configured via "Number of sockets for Socket API usage" in tag list from 1 up to 16.
Number of possible parallel Socket API services	Default: 4 Can be configured via "Number of Socket API Services at DPM level" in tag list from 1 up to 4.
Maximum transmission unit (MTU) size	up to 1500 bytes
Protocols	■ IPv4 protocol ■ TCP ■ UDP 　■ netident - Hilscher specific protocol to configure IP stack. netIdent can be disabled via "LWIP netident behavior" in tag list.
Socket modes	■ Blocking ■ Non/Blocking
UDP ports	25383 (Hilscher netident)

Table 5. Technical data: Socket API

The maximum values for number of possible parallel active sockets and number of possible parallel Socket API services are configuration parameters in the tag list of the firmware.

Each of these features requires resources and the configuration parameters have to be set in order to not exceed the available resource (e.g. RAM) of a device. The same applies for protocol specific configuration parameters of the tag list as well.

All these configuration parameters compete with each other against the same limited available memory.

2.3.2 Restrictions and limitations

As Socket API is not the main functionality of this firmware, the possible transmission rates will be influenced by higher priority communication tasks and can't be guaranteed.

■ IPv6 protocol is not supported

2.4 General firmware features

In addition to protocol and component specific features, which are described in other parts of this document, the following general features are provided by this firmware:

Feature	Description
File system	8.3 FAT file system (not fail-safe)
Diagnosis interface	UART Can be disabled via "UART Diagnostics Interface" in tag list.
Diagnosis interface	USB Can be disabled via "USB Diagnostics Interface" in tag list.

Table 6. General firmware features

2.4.1 IP ports used by the firmware and its components

The firmware uses the following ports for IP communication by default:

Protocol	Ports
UDP	2222 (EtherNet/IP Class 0/1), 44818 (EtherNet/IP Class 3 and UCMM), 25383 (Hilscher netident), 68 (DHCP client), 161 (SNMP)
TCP	44818 (EtherNet/IP encapsulation protocol), 80 (http)

Table 7. Summary of IP ports used by firmware

3 Taglist

The Taglist allows users to configure and customize the protocol firmware. The taglist is part of the firmware binary and can be modified by the taglist editor utility.

Name	Description
LED	This tag is used to modify physical LED connection settings in the firmware.
Remanent Data Responsibility	With this tag, you can adjust whether loading and storing of remanent data is entirely performed by the communication firmware or by the host application.
DDP Mode after firmware startup	With this tag, you can control the DDP mode on firmware startup.
Phy enable timeout after firmware startup	With this tag you can specify a maximum delay until Phys will be activated by firmware.
UART Diagnostics Interface	UART interface of netX Diagnostics and Remote Access component.
USB Diagnostics Interface	USB interface of netX Diagnostics and Remote Access component.
LWIP Ports for IP 0.0.0.0	With this tag you can enable IP ports for usage with Broadcast communication when IP is 0.0.0.0.
LWIP netident behaviour	With this tag, you can adjust whether the firmware shall activate the Hilscher netident protocol, which is build-in in the IP stack, or not.
Socket API Quantity Structure	adjust the resources allocated and provided by the build-in IP stack.
Ethernet/IP Product Information	When this tag is enabled, the contained CIP Product Information will supercede that from the database or packet configuration.

Table 8. Taglists of firmware X170H000.nxf

Note that the Ethernet/IP Product Information tag can only be used in case the protocol stack is configured using a database. For all other configuration methods, this tag is ignored.

4 Third Party Components

Component Name	Component URL	License Type	License URL
libc	TBA	TBA	TBA
lwIP	https://savannah.nongnu.org/projects/lwip/	BSD	https://lwip.fandom.com/wiki/License
MD5	https://github.com/ARM-software/patrace/blob/master/thirdparty/md5/md5.c	proprietary "as-is"	https://github.com/ARM-software/patrace/blob/master/thirdparty/md5/md5.c
newlib	https://developer.arm.com/tools-and-software/open-source-software/developer-tools/gnu-toolchain/gnu-rm/downloads	License types depending on target (not LGPL, not shipping linux targets)	https://sourceware.org/newlib/COPYING.NEWLIB
SNMPv4	SOURCE URL	proprietary "as is"	LICENSE URL

Table 9. Third Party Components

Component Name	Component URL	License Type	License URL